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APPLICANTS

Tsutomu Imoto, Kanagawa, JAPAN;

** CONTINUING DATA *****

This application is a DIV of 09/862,042 05/21/2001 PAT 6,667,538

** FOREIGN APPLICATIONS *****

JAPAN P2000-153445 05/24/2000

verified

[Signature]

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 4	TOTAL CLAIMS 2	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials				

ADDRESS

26263
 SONNENSCHN NATH & ROSENTHAL LLP
 P.O. BOX 061080
 WACKER DRIVE STATION, SEARS TOWER
 CHICAGO, IL
 60606-1080

TITLE

Method of fabricating semiconductor device having semiconductor resistance element

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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